A corpey Docket: 021906-0307405

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Application No.: 10/743,454

Group Art Unit: 2823

Filed: December 23, 2003

Examiner: Estrada, Michelle

Title: METHOD FOR PLANARIZING A SURFACE OF A SEMICONDUCTOR WAFER

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RESPONSE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated August 25, 2004, the following Remarks are made.

12/23/2004 SMINASS1 00000107 033975 10743454 01 FC:1251 120.00 DA